

Active

- L12: (3957) 365/222
- L13: (5736) 365/230.03
- L14: (2023) 365/227
- L15: (2) "6618314"
- L18: (44) (PARENT near2 RICHARD).in.
- L16: (21) (FISCUS near2 TIMOTHY).in.
- L17: (232) (CHAPMAN near2 DAVID).in.
- L19: (452830) restor\$4 or refresh\$4 or (write adj back) or rewrit\$4 or recharge\$4
- L21: (10254) 19 with (partial or full)
- L29: (8545) ((power adj2 down) or (standby) or (stand-by) or (reduc\$4 adj2 power)) same 19
- L30: (628) 21 and 29
- L31: (303) 21 same 29
- L33: (285) (section or quadrant) and 30
- L34: (96) 365/ and L33
- L35: (6360) 19 near5 (partial or full)
- L36: (75) (section or quadrant) near5 35
- L38: (250) 12 and 21
- L39: (19807) (block or bank or subarray or (sub-array) or (sub adj2 block) or sector) with 19
- L40: (507) (block or bank or subarray or (sub-array) or (sub adj2 block) or sector) with 21
- L41: (28) (parity adj2 check\$4) and 40
- L42: (614) (parity adj2 check\$4) and 39
- L43: (218) (block or bank or subarray or (sub-array) or (sub adj2 block) or sector) and 38
- L44: (41) Partial with array with refresh
- L53: (92) 30 and 12
- L54: (63) 30 and 13
- L55: (25) 30 and 14

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Default operator: OR

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	U	I	2	Document ID	Issue Date	Page	Title	Current OR	Current XRe	Retrieval C	Inventor	S	C	P	3
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5251178 A	19931005	7	Low-power integrated circuit memory	365/227	365/222; 365/230.03		Childers, Jimmie D.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5535169 A	19960709	21	Semiconductor memory device	365/230.03	365/222; 365/233;		Endo, Tetsuya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5798976 A	19980825	50	Semiconductor memory device with reduced current consumption	365/222	365/226; 365/230.03		Arimoto, Kazutami	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6381188 B1	20020430	19	DRAM capable of selectively performing self-refresh operation	365/222	365/225.7; 365/227		Choi, Jong-hyun et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6590822 B2	20030708	35	System and method for performing partial array self-	365/222	365/193; 365/230.03;		Hwang, Hyong-Ryol et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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